

Application No.: 09/785,162

Docket No.: 20402-00623-US

**AMENDMENTS TO THE SPECIFICATION**

On page 2, second full paragraph now reads as follows:

As described above, the receiving circuit having jittered characteristics is subjected to either missing of data or double fetch of data. To solve this problem, it is necessary to use a low ~~jittered~~jitter and accurate clock.

On page 2, third full paragraph now reads as follows:

However, according to the above-described conventional data receiving system which necessitates the use of a low ~~jittered~~jitter and accurate clock, the cost will increase.